

Customer No.: 31561
Application No.: 10/707,163
Docket No.: 10659-US-PA

AMENDMENTS

In The Specification:

[0030] To overcome the above deficiencies, each of the voltage terminals is in contact with a common low-resistance conductive interface. Fig. 7 is a schematic diagram showing a connection structure between the voltage terminals of an OLED and a power supply according to one embodiment of this invention. A conductive interface 704 of low-resistance material is placed in connection between voltage terminals ~~[[706]]~~702 and a power supply 706. Voltage drop between the power supply 706 and the voltage terminals 702 is thereby reduced to a small amount, resulting in less power waste. Being externally connected, the conductive material interface 704 does not occupy any panel area.